

E UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Chuen Rong Leu et al.

Assignee:

Bridge Semiconductor Corporation

Title:

METHOD OF MAKING A SEMICONDUCTOR CHIP

ASSEMBLY WITH AN EMBEDDED METAL PILLAR

Serial No.:

10/719,823

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Examiner:

Unknown

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The undersigned attorney is an attorney of record.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on September 2, 2004.

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Respectfully submitted,

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